

ABSTRACT OF THE DISCLOSURE

A bit stream processing apparatus is provided which stores a bit stream in a circular buffer without separately storing a header and data of the bit stream. The bit stream processing apparatus includes: a circular buffer for storing a transmitted bit stream; a first register for indicating a first read point of the bit stream stored in the circular buffer; a first backup register for backing up data stored in the first register; a second register for storing the number of bits to be read from the circular buffer; an adder for adding the data stored in the second register and data stored in a second register; a controller for determining the number of bits to be shifted in response to the output of the adder; a third register for storing data indicative of a number of bits to be ignored from the first read point; and a second backup register for backing up data stored in the third register.

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